



RESPONSE UNDER 37 CFR § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2124  
Docket No.: 1614.1210

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Teruhiko KAMIGATA, et al.

Serial No. 10/053,707

Group Art Unit: 2193

Confirmation No. 7916

Filed: January 24, 2002

Examiner: Todd D. Ingberg

For: INSTRUCTION PROCESSING METHOD FOR VERIFYING BASIC INSTRUCTION  
ARRANGEMENT IN VLIW INSTRUCTION FOR VARIABLE LENGTH VLIW  
PROCESSOR

**AMENDMENT AFTER FINAL REJECTION**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Attention: **BOX AF**

Sir:

This is in response to the Final Office Action mailed August 25, 2005, and having a period for response set to expire on November 25, 2005. Applicants are filing a petition for a one-month extension of time, thereby extending the due date to December 27, 2005 (December 25 and December 26 occurring on a weekend day and holiday, respectively).

Reconsideration of the claims is respectfully requested. The following remarks are respectfully submitted.

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